

Fig. 1 Prior art

FIG. 1 is a block diagram of a prior art control system. The system includes a reference input 'r' which is fed into a summing junction. The output of the summing junction is fed into a block 'G'. The output of block 'G' is the system output 'y'. A feedback path branches off from the output 'y', passes through a block 'H', and returns to the summing junction.

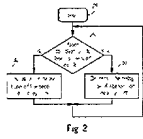
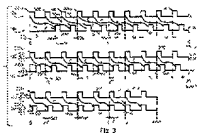
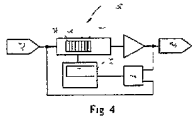
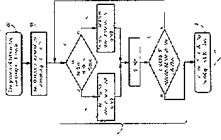


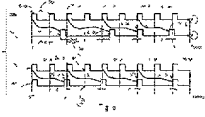
Fig 2



	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99																																																																																																																																																												
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